



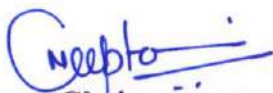
**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Shri Vaishnav Institute of Technology and Science**  
**Choice Based Credit System (CBCS) Scheme in the light of NEP-2020**  
**B.Tech. in Electrical and Electronics Engineering**

**SEMESTER-II (2021-2025)**

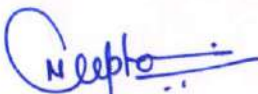
S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	BTMA201	Applied Mathematics II	3	1	0	4	60	20	20	0	0	100
2	BTEE404	Electromagnetic Field Theory	3	1	0	4	60	20	20	0	0	100
3	ML307	Environmental Management and Sustainability	4	0	0	4	60	20	20	0	0	100
4	BTEC102	Fundamentals of Electronics Engineering	3	0	2	4	60	20	20	30	20	150
5	BTCE101	Fundamentals of Civil Engineering	3	0	2	4	60	20	20	30	20	150
6	BTEE103	Electrical Workshop	0	0	6	3	0	0	0	50	50	100
7	BTCS207	Computer Programming II	0	0	2	1	0	0	0	30	20	50
<b>TOTAL</b>			<b>16</b>	<b>2</b>	<b>12</b>	<b>24</b>	<b>300</b>	<b>100</b>	<b>100</b>	<b>140</b>	<b>110</b>	<b>750</b>

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical

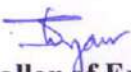
\*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

  
Chairperson

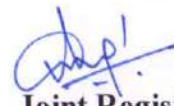
Board of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

  
Chairperson

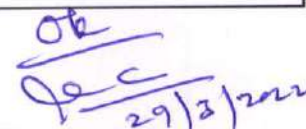
Faculty of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore



Controller of Examinations  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore



Joint Registrar  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

  
29/3/2022

Vice Chancellor  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore